SNJB'S Late Sau K B Jain College of Engineering, Chandwad
Department of Electronics & Telecommunication Engineering

Academic Year: 2023-24	Class: BE
Semester: I	Date:2/9/2023, 17/10/2023, 2/11/2023

Title of Innovation method/activity: Virtual Lab.

Name of Faculty: Prof. Mechkul M. A.

Subject: PLC SCADA and Automation

Objective: To implement logic on PLC virtual lab.

Topic Covered through Activity:

1. Implementation Of Logic Gates
2. Implementation Of PLC Arithmetic Instructions
3. Study of PID controller instruction for a pilot plant

Benefits: Virtual Lab concept to implement said logic in PLC program
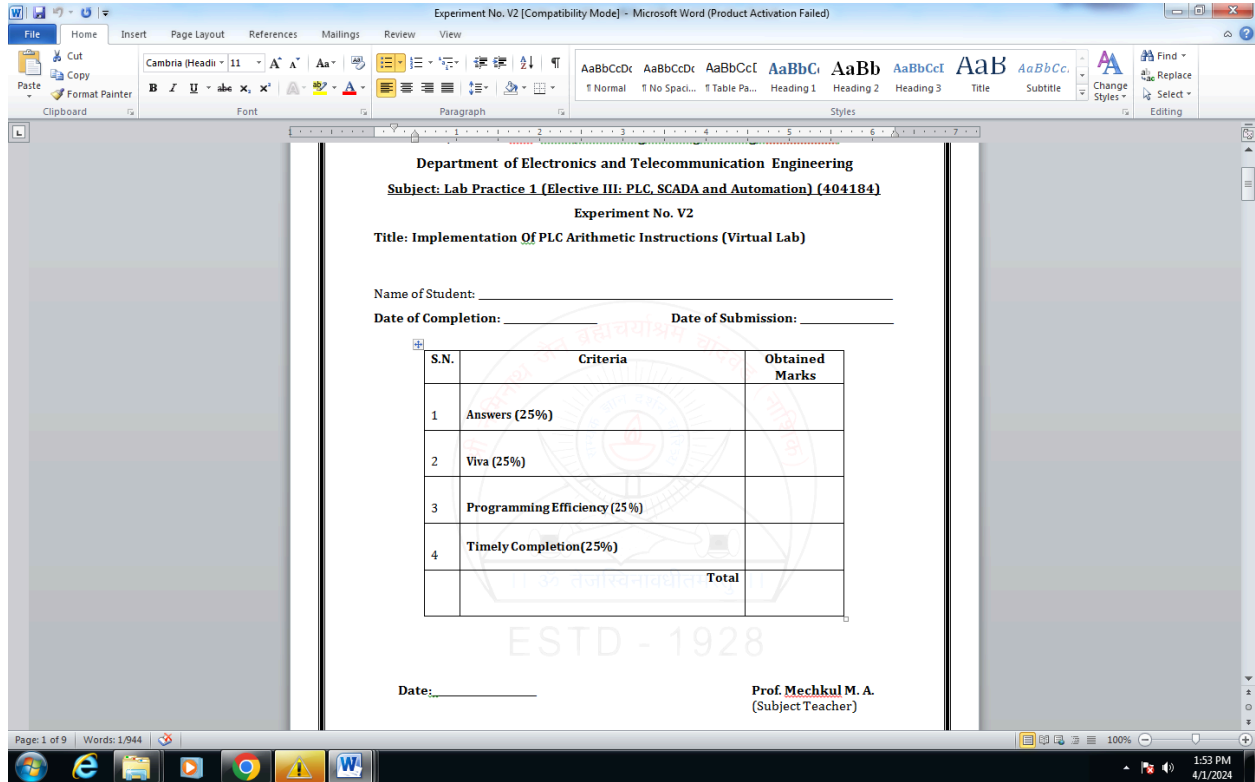
The method:

- Students can access virtual lab through following links:
    1. https://plc-coep.vlabs.ac.in/exp/implementation-logic-gates/

    2. http://plc-coep.vlabs.ac.in/exp7/Theory.html?domain=Electrical%20Engineering&lab=Welcome%20to%20Programmable%20Logic%20Controller%20Lab.
    3. http://plc-coep.vlabs.ac.in/exp8/Aim.html?domain=Electrical%20Engineering&lab=Programmable%20Logic%20Controller%20Lab

- As per procedure given in virtual lab, students prepare the ladder diagram

- Students observe the result through simulation.

Assessment Tools & Rubrics:

- **As per practical rubrics.**

Evaluation Sheet:

Department of Electronics and Telecommunication Engineering

Subject: Lab Practice 1 (Elective III: PLC, SCADA and Automation) (404184)

Experiment No. V2

Title: Implementation Of PLC Arithmetic Instructions (Virtual Lab)

Name of Student: _____

Date of Completion: _____   Date of Submission: _____

S.N.	Criteria	Obtained Marks
1	Answers (25%)	
2	Viva (25%)	
3	Programming Efficiency (25%)	
4	Timely Completion(25%)	
	Total	

Date:_____

Prof. Mechkul M. A.
(Subject Teacher)

Impact Analysis (Show the impact of the activity in examination)

Activity Picture (Add picture of the activity if possible)

For review and critique contact: e-mail address of faculty and HOD

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Subject In charge   **Module Coordinator**   **HoD**

Prof. Mechkul M. A.   **Prof. Baste S. B.**   **Prof. Dr. Agrawal R K**