



# SNJB'S Late Sau K B Jain College of Engineering, Chandwad Department of Electronics & Telecommunication Engineering

| Academic Year: 2022-23 | Class: BE        |
|------------------------|------------------|
| Semester: I            | Date: 29/10/2022 |

**Title of Innovation method/activity:** Virtual Lab.

Name of Faculty: Prof. Mechkul M. A.

**Subject:** PLC SCADA and Automation

**Objective:** To implement logic on PLC virtual lab.

## **Topic Covered through Activity:**

1. Implementation Of PLC Arithmetic Instructions

2. Study of PID controller instruction for a pilot plant

Benefits: Virtual Lab concept to implement said logic in PLC program

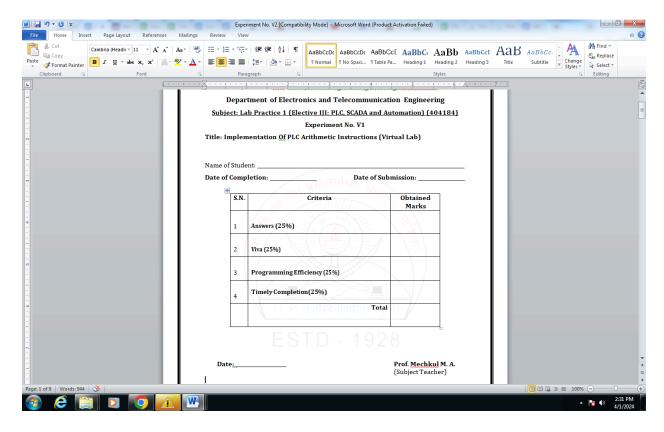
# The method:

- Students can access virtual lab through following links:
  - 1. <a href="http://plc-coep.vlabs.ac.in/exp7/Theory.html?domain=Electrical%20Engineering&lab=Welcome%20to%20Programmable%20Logic%20Controller%20Lab">http://plc-coep.vlabs.ac.in/exp7/Theory.html?domain=Electrical%20Engineering&lab=Welcome%20to%20Programmable%20Logic%20Controller%20Lab</a>.
  - 2. <a href="http://plc-coep.vlabs.ac.in/exp8/Aim.html?domain=Electrical%20Engineering&lab=Programmable%20Logic%20Controller%20Lab">http://plc-coep.vlabs.ac.in/exp8/Aim.html?domain=Electrical%20Engineering&lab=Programmable%20Logic%20Controller%20Lab</a>
- As per procedure given in virtual lab, students prepare the ladder diagram
- Students observe the result through simulation.

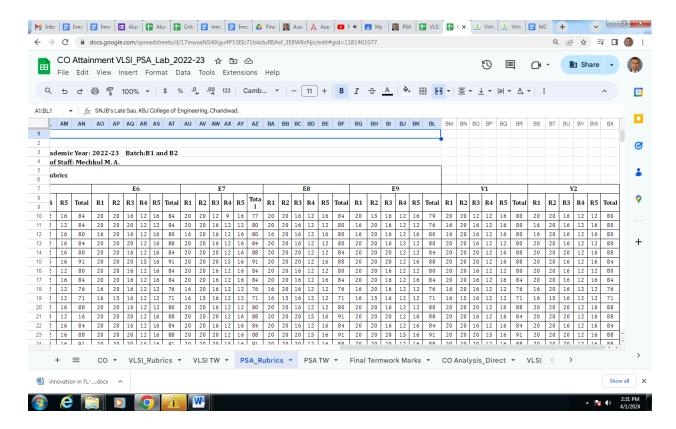
#### **Assessment Tools & Rubrics:**

• As per practical rubrics.

# **Evaluation Sheet:**

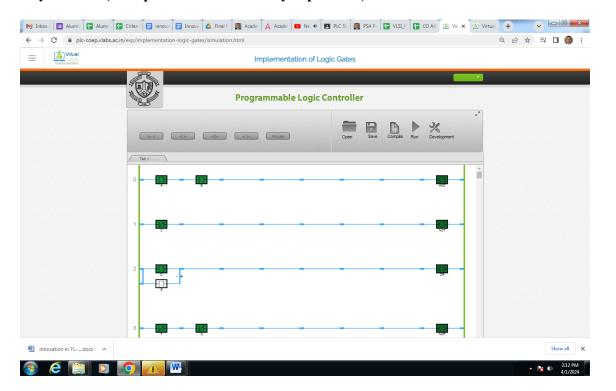


Impact Analysis (Show the impact of the activity in examination)



https://docs.google.com/spreadsheets/d/17inwsaNSiKKgu4P53EIc71tskduflBAsf\_3E8WKsNjc/edit#gid=1 181461077

### **Activity Picture (Add picture of the activity if possible)**



For review and critique contact: e-mail address of faculty and HOD

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Subject In charge Module Coordinator HoD

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