



SNJB'S Late Sau K B Jain College of Engineering, Chandwad
Department of Electronics & Telecommunication Engineering

Academic Year: 2022-23	Class: BE
Semester: II	Date: 27/04/2022

Title of Innovation method/activity: Virtual Lab.

Name of Faculty: Prof. Mechkul M. A.

Subject: PLC and Automation

Objective: To implement logic on PLC virtual lab.

Topic Covered through Activity:

1. Implementation Of PLC Arithmetic Instructions
2. Study of PID controller instruction for a pilot plant

Benefits: Virtual Lab concept to implement said logic in PLC program

The method:

- Students can access virtual lab through following links:
 1. <http://plc-coep.vlabs.ac.in/exp7/Theory.html?domain=Electrical%20Engineering&lab=WELCOME%20to%20Programmable%20Logic%20Controller%20Lab>.
 2. <http://plc-coep.vlabs.ac.in/exp8/Aim.html?domain=Electrical%20Engineering&lab=Programmable%20Logic%20Controller%20Lab>
- As per procedure given in virtual lab, students prepare the ladder diagram
- Students observe the result through simulation.

Assessment Tools & Rubrics:

- As per practical rubrics.

Evaluation Sheet:

Expt. No. 10 Virtual Lab on PID

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SNJB's Late Sau. K. B. Jain College of Engineering, Chandwad.
 Department of Electronics and Telecommunication Engineering
 Subject: Lab Practice IV (Elective III: PLC and Automation) (404194)
 Experiment No. 10

Title: Study of PID controller instruction for a pilot plant (Virtual Lab)

Date of Completion: Date of Submission:

S.N.	Criteria	Possible Marks	Obtained Marks
1	Timely Submission	8.12.16.20	
2	Understanding of experiment/equipments/connection/Program etc.	10.15.20.25	
3	Team Work/involvement in practical	8.12.16.20	
4	Observations, Discussion and Conclusion	6.9.12.15	
5	Oral Questions	8.12.16.20	
	Total		

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 Expt No. 10 Virtual

Grade
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Impact Analysis (Show the impact of the activity in examination)

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<https://docs.google.com/spreadsheets/d/1nTrDCBkjok1PARgPnBFyojxq2PIINGKKZMMGkqOvsYY/edit#gid=1649500246>

Activity Picture (Add picture of the activity if possible)

plc-coep.vlabs.ac.in/exp/implementation-logic-gates/simulation.html

Implementation of Logic Gates

Programmable Logic Controller

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