

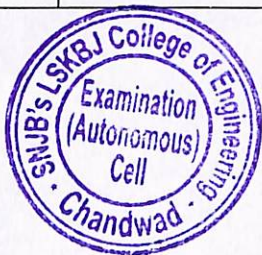


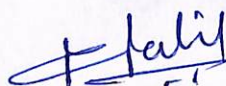
Shree Neminath Jain Brahmacharyashram's
Late Sau. Kantabai Bhavarlalji Jain College of Engineering
Neminagar, Chandwad -423 101 Dist. Nashik.
An Autonomous Institute, Affiliated to Savitribai Phule Pune University, Pune

S.Y.B.Tech. – Electronics & Telecommunication Engineering

Semester End Examination: November 2025 Time Table
(For Regular Students Batch: 2024-2028)

No	Day & Date	Time	Course Name (Code)	Course Code
1	Tuesday 25/11/2025	10.30 am to 01.00 pm	Electromagnetics	24-PCC-ET-2-03
2	Thursday 27/11/2025		Analog Circuits	24-PCC-ET-2-04
3	Saturday 29/11/2025		Analog & Digital Communication	24-PCC-ET-2-05
4	Monday 01/12/2025	10.30 am to 11.30 am	Data Structure (MDM)	24-MDM-CS-2-01




Dr. R. C. Patil
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