

**Late Sau. Kantabai Bhavarlalji Jain**

**College of Engineering**

(An Autonomous Institute Affiliated to Savitribai Phule Pune University, Pune)

Shri Neminath Jain Brahmacharyashram (SNJB) (Jain Gurukul)

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ESTD - 1928

**SNJB**

**Department of E & TC Engineering**

**Board of Study (BoS)**

**Meeting - 04 (41BoSET04)**

Meeting Date: 09/07/2025

Meeting Time: 2 PM

Venue: Online Zoom Platform

**Present Members:**

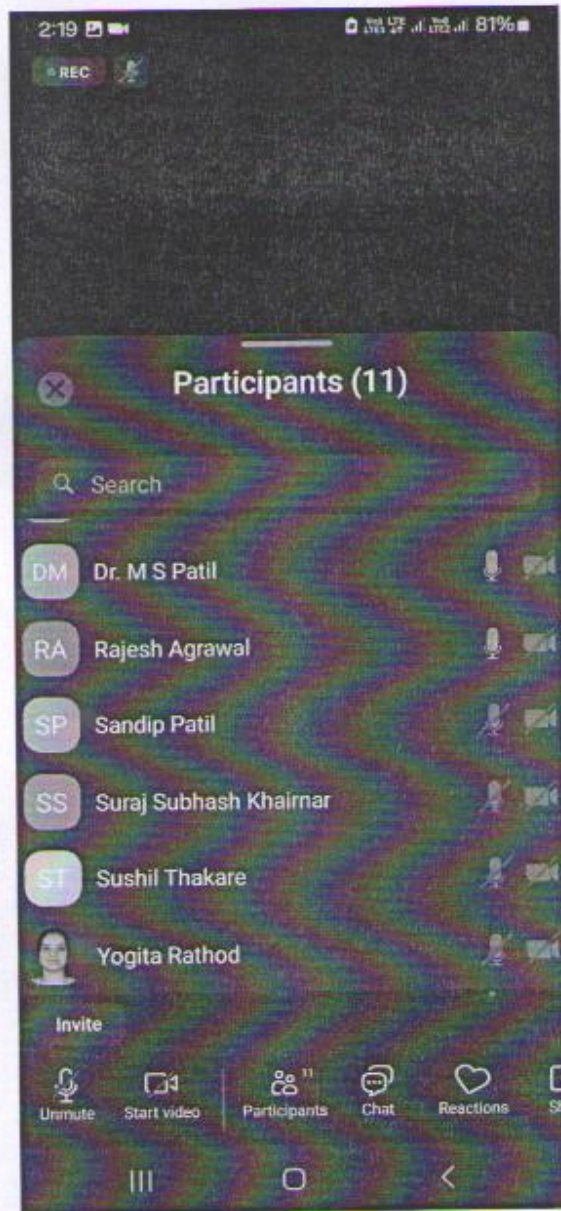
SN	Name	Designation	Institute	Sign/Remark
1	Dr. Agrawal R K	Chairman	Professor & HoD E&TC SNJB CoE, Chandwad	Present
2	Dr. Mrs. Vibha Vyas	Member	HoD, E &TC, CoEP Tech, Pune	Online Via Zoom
3	Dr. M S Patil	Member	HoD, E &TC, RIT, Sakhrale	Online Via Zoom
4	Mr. Rajendra Raut	Member	Assistant General Manager, Fox Solutions, Nashik	Online Via Zoom
5	Dr. Y H Dandawate	Member	Professor, VIIT, Pune	Online Via Zoom
6	Mr. Mechkul M.A.	Member	Assistant Professor, SNJB CoE, Chandwad	Present
7	Mrs. Rathod Y.S.	Member	Assistant Professor, SNJB CoE, Chandwad	Present
8	Mr. Geete R. A.	Member	Assistant Professor, SNJB CoE, Chandwad	Present
9	Mrs. Baste S. B.	Member	Assistant Professor, SNJB CoE, Chandwad	Present
10	Mr. Bamb K. K.	Member	Assistant Professor, SNJB CoE, Chandwad	Absent
11	Mr. Khairnar Sunil	Member	Assistant Professor, SNJB CoE, Chandwad	Present
12	Mr. Khairnar S S	Member	Assistant Professor, SNJB CoE, Chandwad	Present
13	Mr. Patil Sandip U	Member	Assistant Professor, SNJB CoE, Chandwad	Present



**Absent Members:**

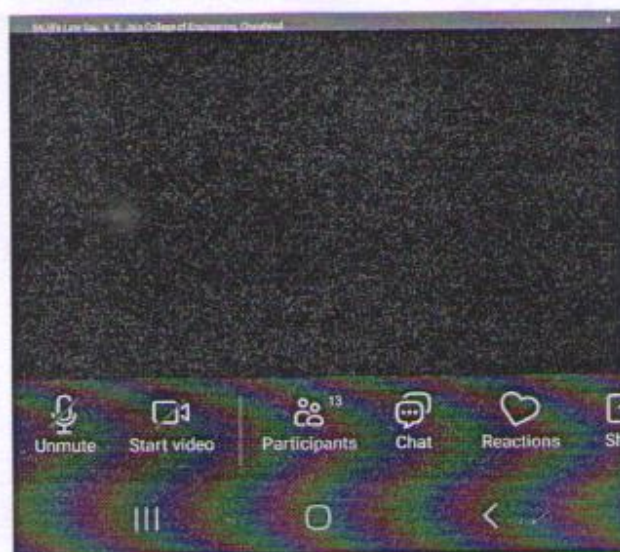
SN	Name	Designation	Institute	Remark	
1	Dr. D S Bormane	Member	Professor, Principal, AISSM, Pune	Absent but informed	
2.	Mr. Kartik Pagar	Member	Tech. Specialist, Cummins Technical Center, Pune	Absent but informed	
3..	Mr. Tejas Bakliwal	Member	Director, Tetavega, Aurangabad	Absent	
4.	Dr. Akshaya Nikumbh	Member	Assist. Professor, IIT, Powai	Absent	
5	Dr. Kokate M.D.	Member	Professor, SNJB CoE, Chandwad	Absent but informed	

Glimpse of meeting





S. No.	Changes Done	Justification for changes done
1	In Unit 1, Add Voltage and Current Divider rule	It is basic tool for circuit analysis and are frequently applied in electronic circuits.
2	In Unit 5, Add Ultrasonic Sensor	The ultrasonic sensor is widely used, in real-time applications such as automation, distance measurement, robotics, and IoT systems.





Agenda No.	Agenda
41BoSET04.01	To discuss the action taken report of BoS meeting no: 03
41BoSET04.02	To approve minor revision in Basic Electrical & Electronics Engineering Theory (ESC) course
41BoSET04.03	To approve minor revision in Basic Electrical and Electronics Engineering Lab (ESC) course
41BoSET04.04	To approve minor revision in Techskill Lab (VSC)
41BoSET04.05	To approve minor revision in Digital Design Theory (PCC) course
41BoSET04.06	To approve minor revision in Digital Design Lab (PCC) course Course code

Detailed discussion and resolution is as follows:

<b>41BoSET04.01: To discuss the action taken report of BoS meeting no: 03</b>	
<b>Discussion</b>	Informed about the action taken on the suggestions received in BoS meeting 3.
<b>Resolution</b>	Accepted.
<b>41BoSET04.02: To approve minor revision in Basic Electrical &amp; Electronics Engineering Theory (ESC) course</b>	
<b>Discussion</b>	Add Voltage and Current Divider rule and Ultrasonic Sensor. It is a basic tools for circuit analysis and IS frequently applied in electronic circuits. The ultrasonic sensor is widely used in real-time applications such as automation, distance measurement, robotics, and IoT systems.
<b>Resolution</b>	The revision is approved by the BoS.
<b>41BoSET04.03: To approve minor revision in Basic Electrical and Electronics Engineering Lab (ESC) course</b>	
<b>Discussion</b>	Add practical on "Determine Efficiency and Regulation of a Single-Phase Transformer". This practical introduces students to essential performance parameters of transformers, such as efficiency and voltage regulation. Removed Case study on applications of different types of motors - Stepper, Servo, BLDC.
<b>Resolution</b>	The revision is approved by the BoS.
<b>41BoSET04.04: To approve minor revision in Techskill Lab (VSC)</b>	

<b>Discussion</b>	Add "Study of different types of electrical wires, their applications & wiring standards related to fire safety". And Hands-on practice of residential wiring & wiring board. This provides students with essential knowledge of wiring types, safety standards, and real-world wiring practices. Remove assignment on 1) Fault Finding and repairing of LED Bulb, Door Bell 2) Fault Finding and repairing of Charger, Hand Blender.
<b>Resolution</b>	The revision is approved by the BoS.
<b>41BoSET04.05: To approve minor revision in Digital Design Theory (PCC) course</b>	
<b>Discussion</b>	Add 1'S and 2'S Complement topic. Its fundamental number representations, used for performing binary subtraction and arithmetic in digital systems. Important as per the Gate Syllabus also. These concepts are directly applied in the design of Arithmetic Logic Units (ALUs), adders, and subtractors.
<b>Resolution</b>	The revision is approved by the BoS.
<b>41BoSET04.06 &amp; 7: To approve minor revision in Digital Design Lab (PCC) course code</b>	
<b>Discussion</b>	Courses Codes are updated as per institute policy. To keep track of revisions.
<b>Resolution</b>	The revision is approved by the BoS.



*Dr. R K Agrawal*  
**Dr. R K Agrawal**  
**BoS CHAIRMAN**  
**BOARD OF STUDIES E&TC ENGINEERING**  
**LSKBJ COLLEGE OF ENGINEERING**  
**Chandwad Dist. Nashik**