

Late Sau. Kantabai Bhavarlalji Jain
College of Engineering

(An Autonomous Institute Affiliated to Savitribai Phule Pune University, Pune)

Shri Neminath Jain Brahmacharyashram (SNJB) (Jain Gurukul)

Neminagar, Chandwad - 423101, Dist. Nashik (MS, India).

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ESTD - 1928

SNJB

Department of E & TC Engineering

Board of Study (BoS)

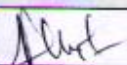







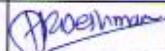
Meeting - 01 (41BoSET01)

Meeting Date: 27/03/2024

Meeting Time: 2 PM

Venue: Online Zoom Platform

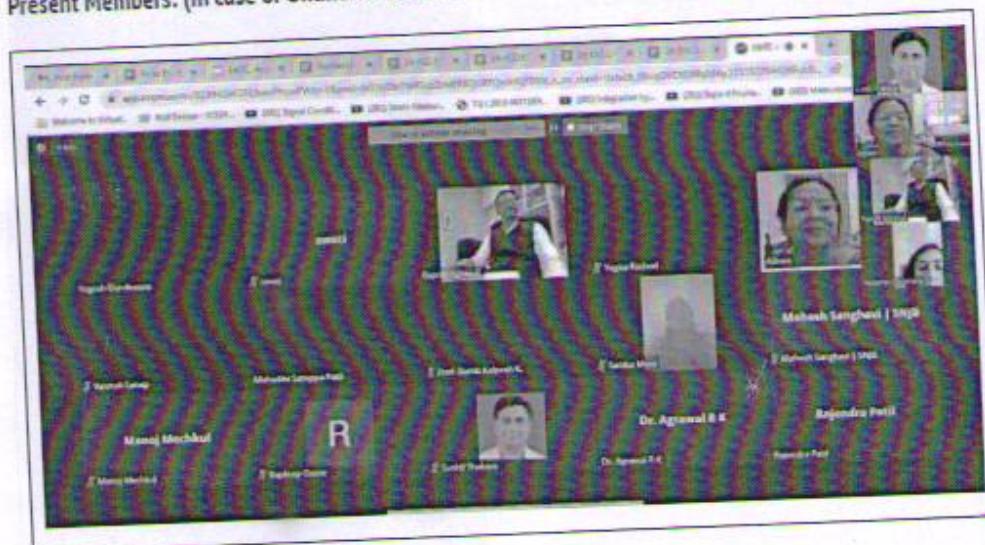
Present Members:

SN	Name	Designation	Institute	Sign/Remark
1	Dr. Agrawal R K	Chairman	Professor & HoD E&TC SNJB CoE, Chandwad	
2	Dr. Mrs. Vibha Vyas	Member	HoD, E &TC, CoEP Tech, Pune	Online Via Zoom
3	Dr. M S Patil	Member	HoD, E &TC, RIT, Sakhrate	Online Via Zoom
4	Dr. Y H Dandawate	Member	Professor, VIIT, Pune	Online Via Zoom
5	Mr. Rajendra Raut	Member	Assistant General Manager, Fox Solutions, Nashik	Online Via Zoom
6	Dr. Kokate M.D.	Member	Professor, SNJB CoE, Chandwad	
7	Mr. Mechkul M.A.	Member	Assistant Professor, SNJB CoE, Chandwad	
8	Mrs. Rathod Y.S.	Member	Assistant Professor, SNJB CoE, Chandwad	
9	Mr. Thakare S.S.	Member	Assistant Professor, SNJB CoE, Chandwad	
10	Mr. Geete R. A.	Member	Assistant Professor, SNJB CoE, Chandwad	
11	Mrs. Baste S. B.	Member	Assistant Professor, SNJB CoE, Chandwad	
12	Mr. Bamb K. K.	Member	Assistant Professor, SNJB CoE, Chandwad	
13	Mr. Kshirsagar G.R.	Member	Assistant Professor, SNJB CoE, Chandwad	
14	Ms. Sanap V. A.	Member	Assistant Professor, SNJB CoE, Chandwad	
15	Mrs. Deshmane H. R.	Member	Assistant Professor, SNJB CoE, Chandwad	

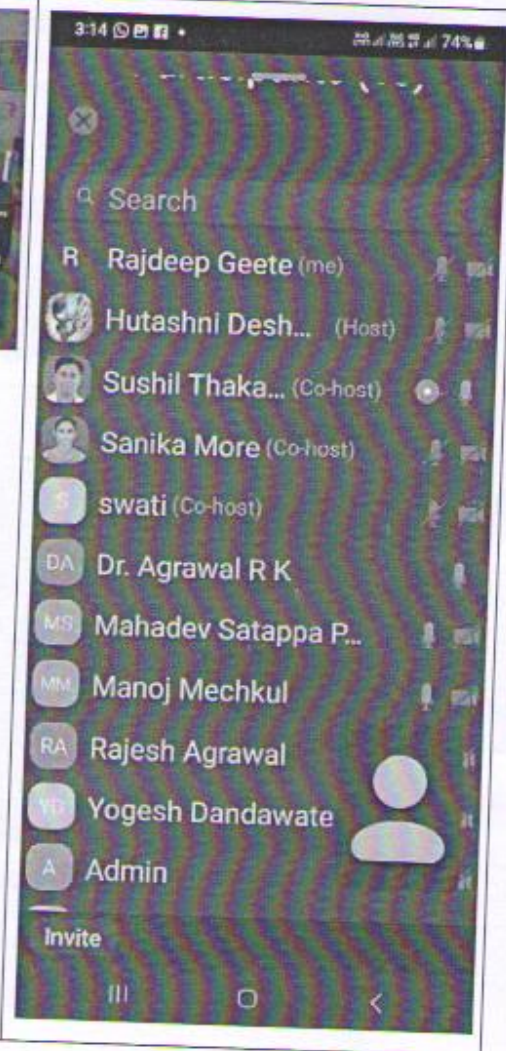
Absent Members:

SN	Name	Designation	Institute	Remark
1	Mr. Tejas Bakliwal	Member	Director, Tetavega, Aurangabad	With prior permission of Chairman BoS

Present Members: (In case of Online Screenshot of Present Members)



Glimpse of meeting-

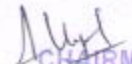


Agenda	
41BoSET01.01	To welcome and introduce distinguished members.
41BoSET01.02	Brief introduction about the institute & department.
41BoSET01.03	To discuss and approve the draft syllabus of Basic Electrical & Electronics (TH & PR), proposed by the department for the first year of engineering.
41BoSET01.04	To discuss and approve the draft syllabus of Digital Design (TH & PR), proposed by the department for the first year of engineering.
41BoSET01.05	To discuss and approve the draft syllabus of Techskill module III, proposed by the department for the first year of engineering.
41BoSET01.06	To review and approve the draft curriculum structure from the Second Year to the Final years of E & TC engineering program.
41BoSET01.07	To review and approve the draft curriculum structure of the third Year of E & TC engineering program.
41BoSET01.08	To review and approve the draft curriculum structure of the final Year of E & TC engineering program.
41BoSET01.09	To review and approve the draft Program Electives, Open Electives, Multidisciplinary Minor, Double Minor and Honors Bucket of E & TC engineering program.

Detailed discussion and resolution is as follows:

41BoSET01.01: To welcome and introduce distinguished members.	
Discussion	Dr. R K Agrawal has introduced and welcomed the distinguished members: Dr. Vibha Vyas, Dr. M S Patil, Dr. Y H Dandawate, Mr. Rajendra Raut and thanked for accepting the invitation to join the board.
Resolution	All agreed
41BoSET01.02: Brief introduction about the institute & department.	
Discussion	Dr. R K Agrawal has introduced the SNJB Gurukul, CoE and the E & TC Dept.
Resolution	All agreed
41BoSET01.03: To discuss and approve the draft syllabus of Basic Electrical & Electronics (TH & PR), proposed by the department for the first year of engineering.	
Discussion	Mr. R A Geete, Assistant Professor, E&TC dept., presented the draft syllabus of Basic Electrical & Electronics (TH & PR). The members suggested improving the bloom's level for course outcomes.
Resolution	Draft syllabus of Basic Electrical & Electronics (TH & PR) is accepted with above modifications.
41BoSET01.04: To discuss and approve the draft syllabus of Digital Design (TH & PR) proposed by the department for the first year of engineering.	
Discussion	Mr. S S Thakare, Assistant Professor, E&TC dept., presented the draft syllabus of Digital Design (TH & PR). The members suggested John Walkerley and Thomas L. Floyd books as a reference book for Digital Design.
Resolution	Draft syllabus of Basic Electrical & Electronics (TH & PR) is accepted with above modifications.
41BoSET01.05: To discuss and approve the draft syllabus of Techskill module III, proposed by the department for the first year of engineering.	

Discussion	Mr. R A Geete, Assistant Professor, E&TC dept., presented the draft syllabus of Techskill module III (PR). The members suggested adding an Arduino & Microcontroller if possible. But it was pointed out that the lab course is aimed towards repairing home appliances.
Resolution	Draft syllabus of Techskill module III (PR) is accepted as it is.
41BoSET01.06: To review and approve the draft curriculum structure of the second Year of E & TC engineering program.	
Discussion	Mrs. Y S Rathod, Assistant Professor, E&TC dept., presented the draft structure of the second year: sem III & SEM IV.
Resolution	Draft syllabus of Techskill module III (PR) is accepted as it is.
41BoSET01.07: To review and approve the draft curriculum structure of the third Year of E & TC engineering program.	
Discussion	Mrs. S B Baste, Assistant Professor, E&TC dept., presented the draft structure of the third year: sem V & SEM VI.
Resolution	Draft syllabus of Techskill module III (PR) is accepted as it is.
41BoSET01.08: To review and approve the draft curriculum structure of the final Year of E & TC engineering program.	
Discussion	Mr. S S Thakare, Assistant Professor, E&TC dept., presented the draft structure of the final year: sem VII & SEM VIII.
Resolution	Draft syllabus of Techskill module III (PR) is accepted as it is.
41BoSET01.09: To review and approve the draft Program Electives, Open Electives, Multidisciplinary Minor, Double Minor and Honors Bucket of E & TC engineering program.	
Discussion	Mr. M A Mechkul, Assistant Professor, E&TC dept., presented the draft buckets of Program Electives, Open Electives, Multidisciplinary Minor, Double Minor and Honors courses. The members suggested adding a VLSI domain bucket in PEC.
Resolution	VLSI domain bucket is added in PEC and the draft is accepted.


CHAIRMAN
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